



#18 Supp IDS  
M. Brunson  
10/8/02

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of )  
Shinji TAKEDA et al. ) Atty. Docket No.: TM&K0007  
Serial No. 09/785,436 ) Group Art Unit: 2829  
Filed: February 20, 2001 ) Examiner: GEYER, S.  
For: SEMICONDUCTOR DEVICE AND )  
PROCESS FOR FABRICATION ) Date: September 20, 2002  
THEREOF )

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**SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT**

Assistant Commissioner for Patents  
Washington, D.C. 20231

Sir:

This is a Supplemental Information Disclosure Statement with regard to the above-identified application. Copies of each of the documents listed in the attached Form PTO/SB/08B are attached. Document A was cited in and its relevance stated in the European Search Report (a copy of which is attached as document B) of a corresponding European Patent Application. Accordingly, no further comment regarding the relevance of the documents is believed necessary.

It is respectfully requested that the attached documents be considered and officially cited, and that a copy of the Form PTO/SB/08B, initialed by the Examiner to indicate that the documents have been considered, be returned to the applicants.

Serial No. 09/785,436

The Commissioner is hereby authorized to charge the government fee of \$180.00 for this information disclosure statement and any fee deficiency or any additional fees which may be further required in this application, or credit any overpayment to Deposit Account No. 501281, under an order number which corresponds to the above attorney docket number.

Respectfully submitted,

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